

Digital CMOS Platform Overview

0.35 μ m, 0.18 μ m, 0.13 μ m Nodes

With our CMOS/RFCMOS offering, we support designers who are seeking volume production at 0.18-, 0.16-, and 0.13-micron nodes. We provide a flexible and highly customizable process, a world renowned modeling kit, and best-in-class PDK support to achieve industry-leading yields, maximum performance, cost-effectiveness and fast time-to-market. We offer a wider 0.13-micron process technology than competitive offerings and our unique ability to modify the process in a very short time allows us to meet our customers' aggressive timelines. Decades of CMOS technology expertise enables us to deliver an optimum solution to our customers with outstanding service and support. We are able to meet high volume needs up to 50K wafer/month capacity.

- 0.35 μ m, 0.18 μ m, 0.13 μ m nodes
- Aluminum and Copper BEOL
- Up to 8 Levels of Metal
- I/Os of 5V, 3.3V, 2.5V, 1.8V
- Large Standard Cell Library from ARM and Virage
- Memories including SRAM and NVM/Flash
- I/O Library Including HSTL, SSTL, PCI-X, LVDS, GPIO
- Bond Options including Pad Over Logic (POL) and Redistribution Layer (RDL)

Features	0.35	0.18	0.13
Location	Fab1, Fab3	Fab2, Fab3	Fab2, Fab3
CMOS FET	0.35 μ m	0.18 μ m	0.13 μ m
Metal Levels	Up to 5	Up to 6	Up to 8
BEOL	Al/USG	Al/USG	Al/USG or Cu/FSG (Fab2)
Logic	3.3V	1.8V	1.2V SL (Standard Logic)
	Single/Dual Vt	Single/Dual Vt	Dual/Triple Vt
			1.2V LL (Low Leakage)
			Dual Vt
			1.0V LV (Low Voltage)
			Dual Vt

Customer Service and Support

- **eBizz website:** <https://online.jazzsemi.com>
- **eTower website:** <https://etower.towersemi.com>
- File Exchange for design kits and online documentation
- Online Tape-Out System
- Online Help Ticket System
- Manufacturing status, logistics and inventory management
- Dedicated Sales and Engineering Support

Digital Design Kit Features

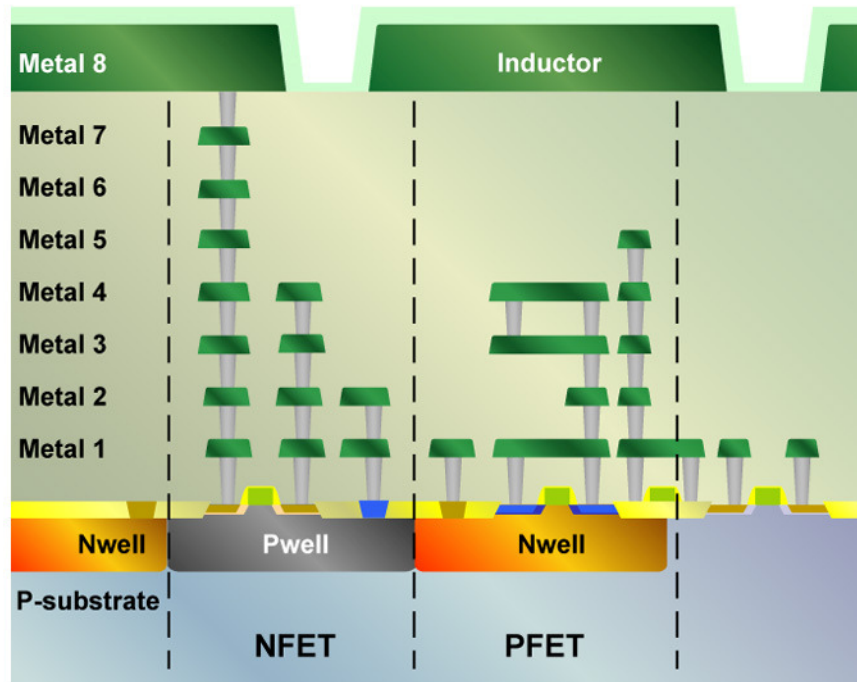
- Assura™
- Support for Mentor® Calibre interactive/XRC
- Support for Spectre, HSPICE simulators
- Includes basic ESD structures

ASIC Library Views and Features

- Standard Cell Libraries
- I/O Libraries
- Synopsys and Cadence ASIC Flows
- Memory Generators

Supported Models

- **MOSFETs:** Scalable BSIM3/PSP models, mismatch, statistical and noise models
- **LDMOS:** MM20 + Jazz Rdrift



About TowerJazz

Tower Semiconductor Ltd. (NASDAQ: TSEM, TASE: TSEM), the global specialty foundry leader and its fully owned U.S. subsidiary Jazz Semiconductor, operate collectively under the brand name TowerJazz, manufacturing integrated circuits with geometries ranging from 1.0 to 0.13-micron. TowerJazz provides industry leading design enablement tools to allow complex designs to be achieved quickly and more accurately and offers a broad range of customizable process technologies including SiGe, BiCMOS, Mixed-Signal and RFCMOS, CMOS Image Sensor, Power Management (BCD), and Non-Volatile Memory (NVM) as well as MEMS capabilities. To provide world-class customer service, TowerJazz maintains two manufacturing facilities in Israel and one in the U.S. with additional capacity available in China through manufacturing partnerships. For more information, please visit www.towerjazz.com.